



# AMP4061P SOLID STATE PULSE HIGH POWER AMPLIFIER

## FEATURES

- Rack mounted cabinet
- High power LDMOS pulse devices
- Suitable for VHF high power linear pulse applications
- Built-in Ethernet, Control and Protection circuits
- High reliability and ruggedness

## ELECTRICAL SPECIFICATIONS: 50Ω, 25°C

Parameter	Specification	Notes
Operating Frequency Range	150 - 225 MHz	
Peak Output Power	10 KWatt Min	15% Duty Cycle
Input Power	0 dBm Nom	Pulsed
Saturated Gain	70 dB Min	
Gain Adjustment Range	20 dB Min	
Pulse Characteristics	<b>Duty</b>	
	15 % Max	<b>Width</b> 4 - 650 μSec
		<b>Rise / Fall</b> <100 nS
Pulse Droop	1.0 dB Max	@ 650 μSec
Input / Output VSWR	2 : 1	Relative to 50 Ohm
Harmonics 2 <sup>nd</sup> / 3 <sup>rd</sup>	-15 / -12 dBc Max	Internal Harmonics Filter
Out of Band Spurious levels	-50 dBc Max	
Load VSWR protection	2.5 : 1 Max	At rated Pout
Input Power Protection	+ 5 dBm Max	<10 Sec without damage
Gate Control Inputs	TTL - gating pulse must precede RF by 2 μSec	Optional
AC Input Voltage	208 VAC, 3 Phase, 60 Hz	
AC Power Consumption	6 KW Avg. Max	

## ENVIRONMENTAL CHARACTERISTICS

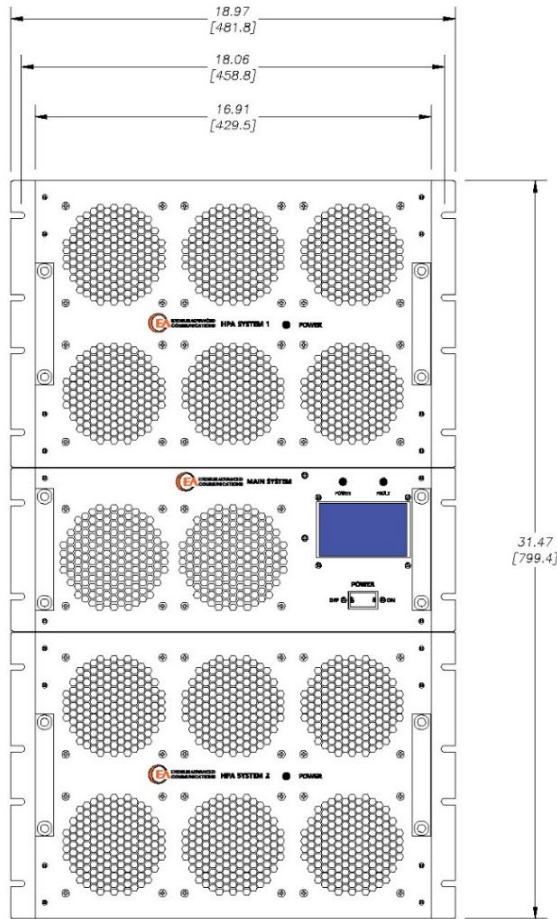
Parameter	Specification	Notes
Operating Ambient Temperature	0 to +50 °C	
Relative Humidity	5 - 95 %	Non-condensing
Shock and Vibrations	MIL-STD-810F	Designed to meet
Altitude	3,000 meters	

## MECHANICAL SPECIFICATIONS

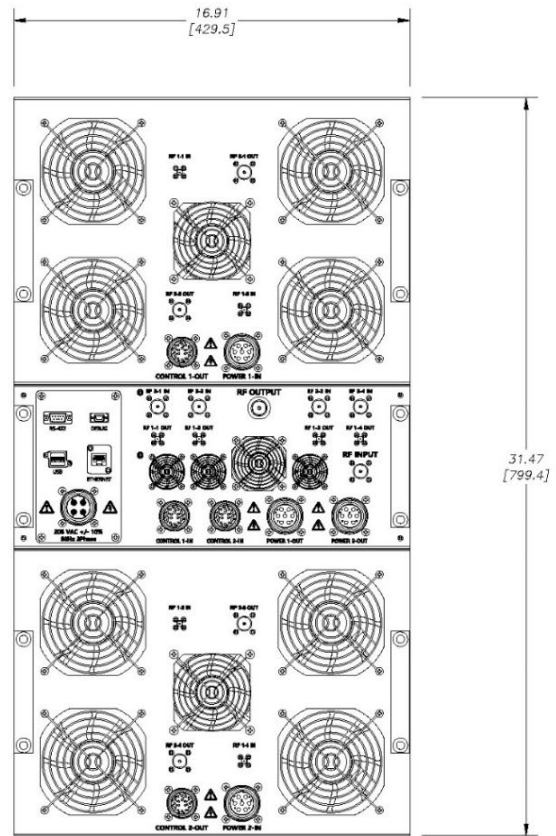
Parameter	Specification	Notes
Dimensions	See preliminary outline drawing	18U, 19" rack
Weight	<450 lb. Nom	
RF Input Connector	Type N-F	Rear panel
RF Output Connector	7/16 F	Rear panel
AC Power	MS3122E14-5P	Or similar
Transmit Gating Connector	MS circular or similar	
<b>STANDARD:</b> Digital Monitor & Control FWD, REV, VSWR, GAIN, ALC, V & I, TEMP	Ethernet RJ-45 TCP/IP, RS422/485, USB	
Cooling	Built in forced air Cooling	Front Inlet, Rear outlet
Elapse Hour Meter	Internally tracked via software	Ethernet status message

## OUTLINE DRAWING

### 1. Front and Rear Panel Outlines

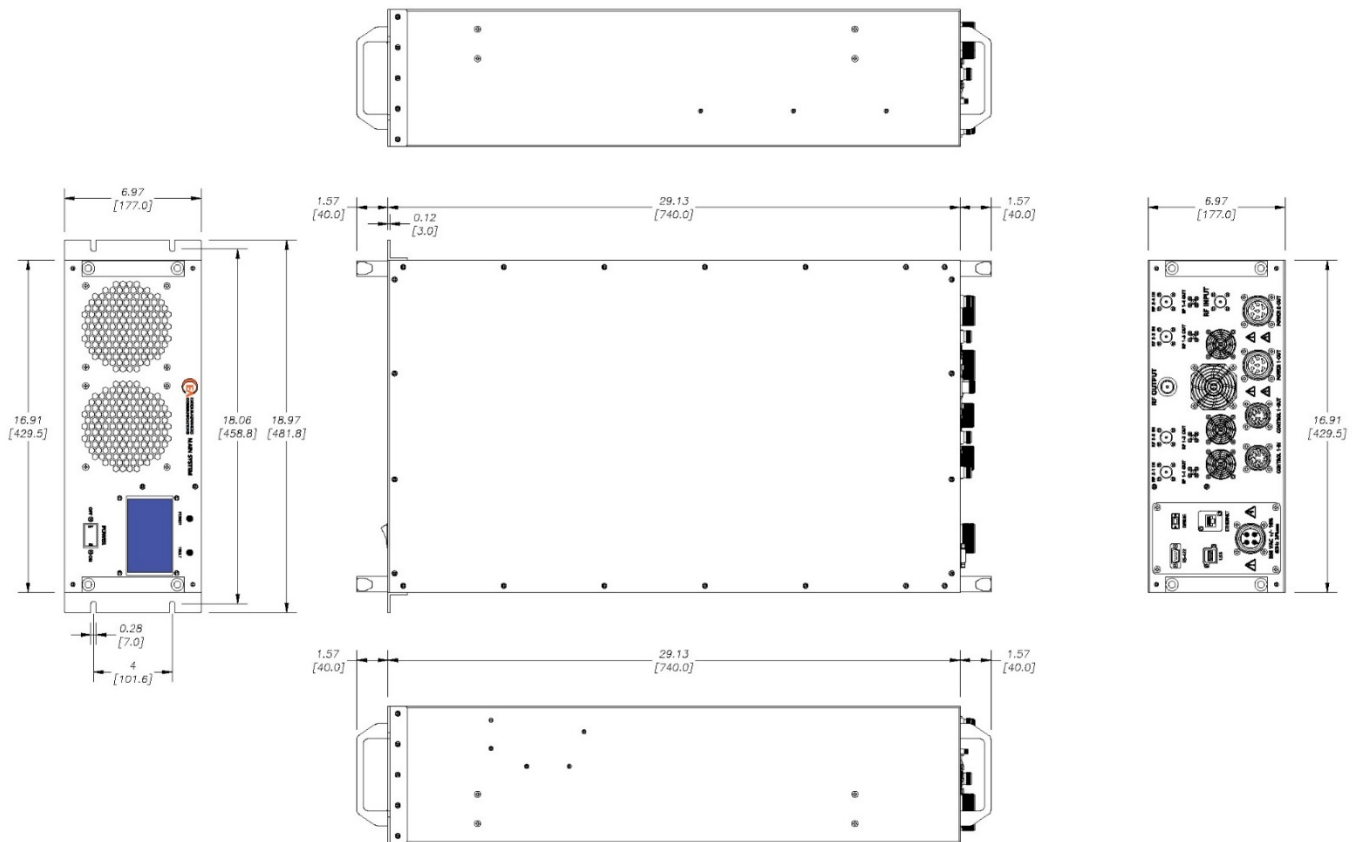


FRONT

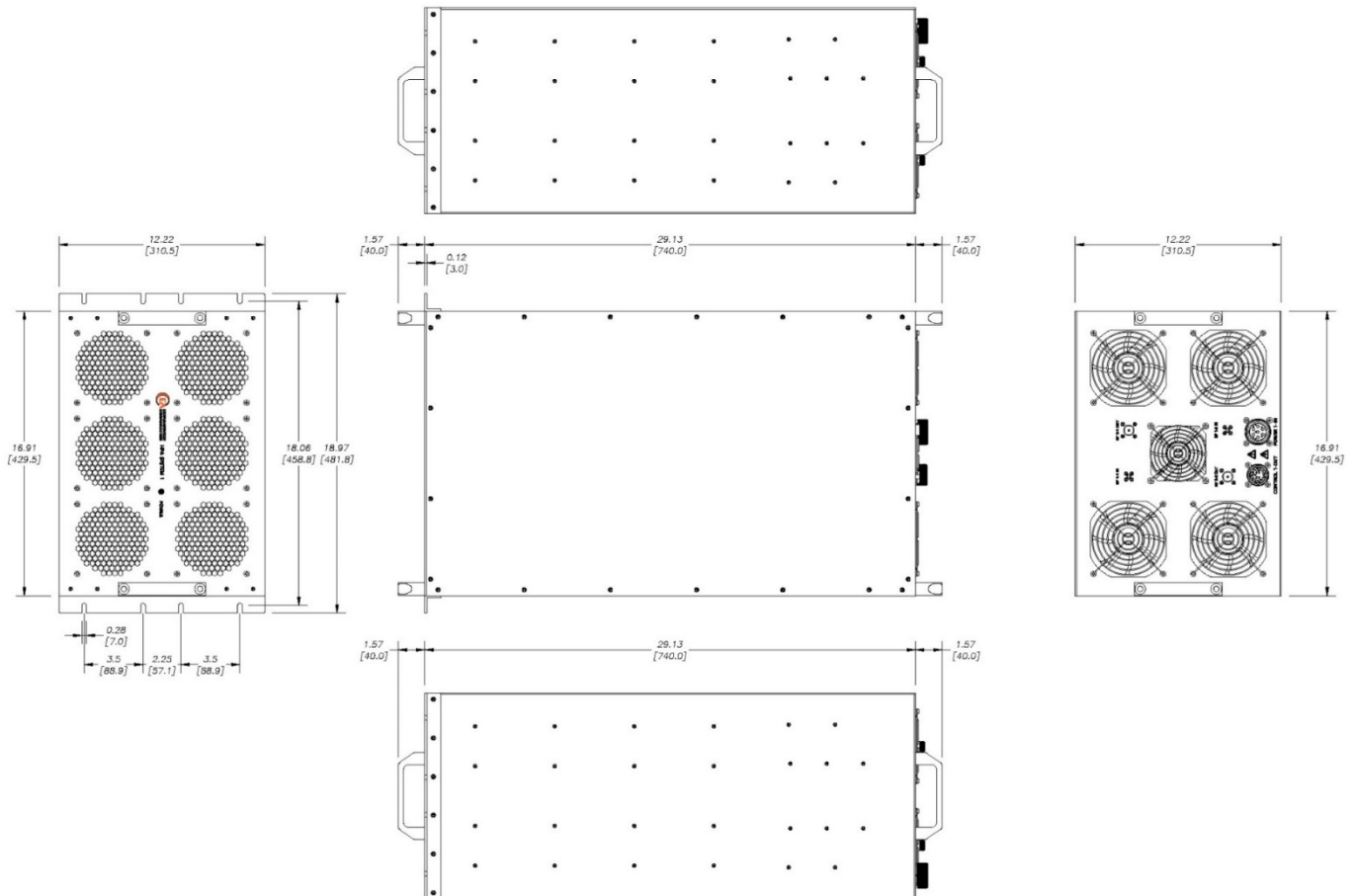


REAR

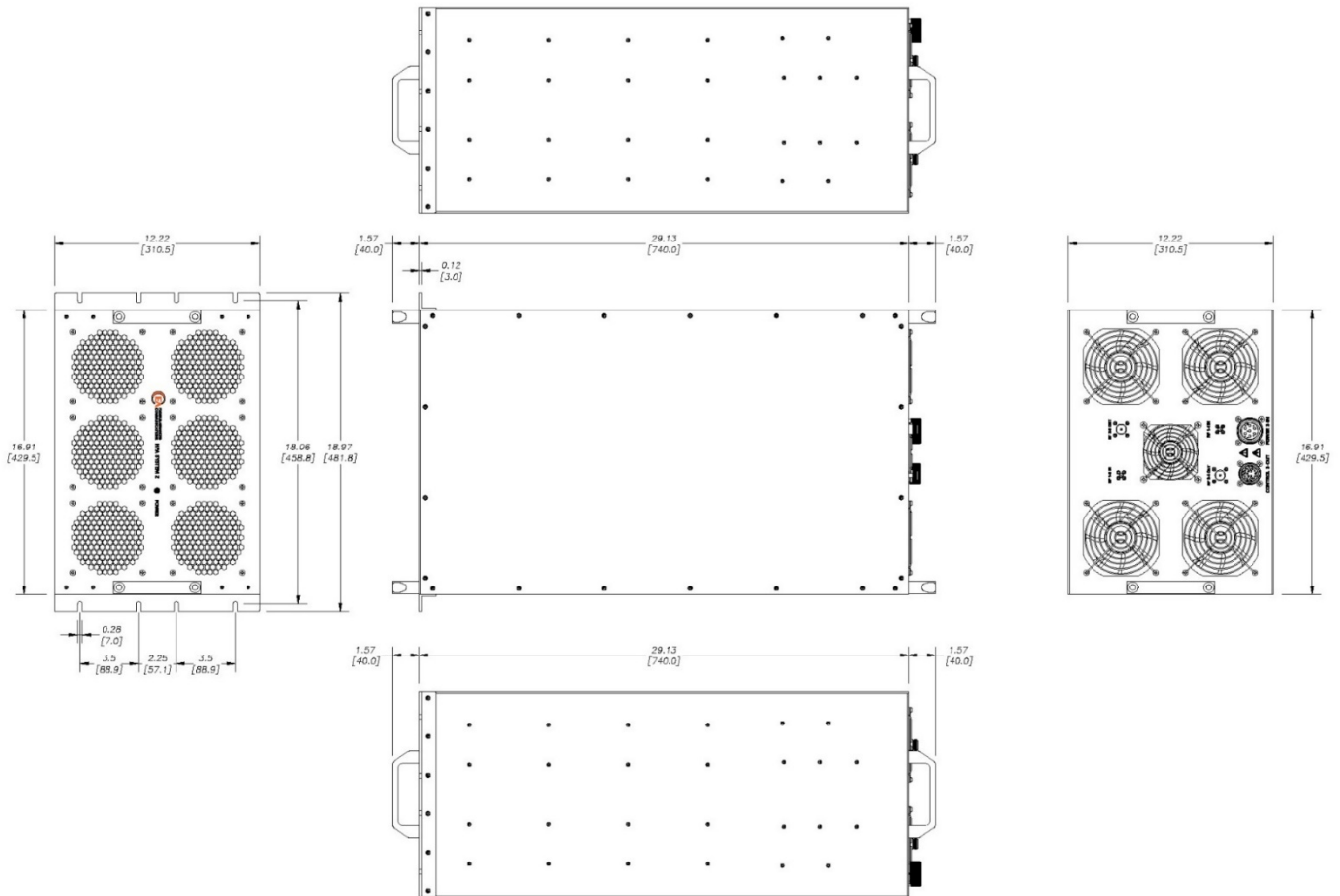
## 2 Main System Outline



### 3 HPA System 1 Outline



## 4 HPA System 2 Outline



## 5 System Complete Layout

